

Indira Gandhi Delhi Technical University For Women

Department of Electronics and Communication Engineering (Formerly Indira Gandhi Institute of Technology)

(Established by Govt. of Delhi vide Act 09 of 2012)

16th September 2019

<u>Walk-in-Interview for the post Guest Lecturer and Research Associate under SMDP-C2SD Project</u> (On Contract Basis)

Applications are invited from research oriented candidates for the position of Guest Lecturer and Research Associate under the project titled "Special Manpower Development Program for Chip-to-System Design (C2SD)" sponsored by Ministry of Electronics and Information Technology, Government of India. Detail of the said posts is as follows:

S. No.	Post	Essential Qualification	Emoluments
1	Research Associate (One Post)	B.E./B.Tech./M.Sc./M.Tech. in Electronics & Communication/ Electrical & Electronics/VLSI Design with first class, or its equivalent grade from a reputed Institute. Desirable Qualification: A sound knowledge of VLSI and	Rs. 20,000/month for one post
2	Guest Lecturer (One Post)	relevant software Cadence/Synopsys is must alongwith the knowledge about chip layout, DRC, PEX analysis. PCB Design tool exposure, Verification and Testing of the chip. OR A sound knowledge of Image Processing, Machine Learning and Python is must.	Rs. 40,000/month for another post

General Terms and Conditions:

- 1) Both male and female candidates can apply for the above-mentioned posts.
- 2) The candidates applying against this advertisement will be considered for both the advertised posts.
- 3) The candidate should bring a copy of the duly filled form attached alongwith and a latest copy of their bio-data at the time of interview.
- 4) A photocopy of all the supporting documents should be attached with the bio-data and originals may be produced for verification purpose.
- 5) The position is purely on temporary basis for a period of 06 months. The duration can be extended depending upon the satisfactory performance and provision under the project.
- 6) Selection committee reserves the right to fix suitable criteria for selection of candidates and the final selection of the candidate against the RA/GL post.
- 7) No TA/DA will be provided to candidates for appearing in the Interview.
- 8) The candidates are requested to regularly check the university website for further proceedings, if any.
- 9) Any queries can be sent at hodece@igdtuw.ac.in
- 10) Tentative Date & Time for Interview: 4th October 2019 at 11.00 a.m.

Room No. 236, First Floor

Committee Room

Department of Electrical Engineering,

IIT Delhi

Chief Investigator SMDP-C2SD Project

INDIRA GANDHI DELHI TECHNICAL UNIVERSITY FOR WOMEN

KASHMERE GATE, DELHI – 100006

Application Form For Guest Lecturer & Research Associate Post

Special Manpower Development Program for Chips to System Design Project Funded by Ministry of Electronics and Information Technology.

	Government of India							
	 Fill in all the information in block letters only. Attach separate sheet in case of insufficient space in any column. Attach copies of all educational qualification and experience certificates only. 							
1.	Post appl	lied for	:					
2.	Candidat	teøs nan	ne in full:					
3.	Address	for corr	respondence:					
4.	Permane	nt resid	ential address:					
5.	Telephor	ne No. ((with STD code):					
	Mobile N	No.:						
	Fax No.	(with S	TD code)					
6.	E-mail address:							
7.	Date of Birth:							
8.	Husbandøs name:							
9.	Marital status:							
10.	. Sex:							
11.	1. Nationality:							
12.	Category	/:						
13.	3. Designation and complete postal:							
	Address	of curre	ent employer:					
14.	Educatio	nal qua	lifications:					
	Examina	tion	University/Board	Year of Passing/ Award	Division	%age of marks/grade	Subjects	
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Examination	University/Board	Year of Passing/ Award	Division	%age of marks/grade	Subjects

- 15. Projects handled/Completed during B.Tech. & M.Tech. (Title, Abstract & Supervisor Name)
- 16. Month & year of passing of NET/GATE or similar test: --
- 17. Field of specialization:
- 18. Experience (Please start with the latest)

Name of the	Post held	Pay scale	Basic	Period		Nature of work	Reasons
institution				From	То	rvature or work	for leaving

((b) Research:							
	(i) Number of years:							
	(ii) Name of institution where work done:							
20.	Number & Details of Publ	ications:						
21.	Languages Known	1	1					
	Languages Known	Read	Write	Speak				
	(i)							
	(ii)							
22.	Present Basic Pay							
	Period required for joining	, if selected:						
			Schools/Conferences Atte	nded/Organized: (in the				
	 Workshop/Training Programmes/Summer/Winter Schools/Conferences Attended/Organized: (in the last three years) 							
25.	. Any other relevant information you wish to give in support of your candidature:							
26.	Name and address of two persons to whom references can be made:							
	(1)							
	(2)							
27.	Certified that the informat	ion given by me in this ap	pplication form is complet	e and correct to the best				
	of my knowledge & belie	f and nothing has been co	oncealed there from. I also	understand that in case				
	any information is found to be false, my services shall be liable to be terminated without notice.							
	There werd the instructions and evidelines for the second date.							
	I have read the instructions and guidelines for the candidates.							
	Date:							
	Place:							
	Signature of the candidate							

19. Teaching/Research Experience:

(a) Total teaching experience (UG & PG both):